

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S13	29	S12 and (bump or ball or c4)	US-PGPUB; USPAT	OR	ON	2007/01/03 17:49
S14	29	S12 and (bump or ball or c4)	US-PGPUB; USPAT	OR	ON	2007/01/03 17:49
S19	13	257/777.fs. and S18	US-PGPUB; USPAT; USOCR	OR	ON	2007/01/03 18:13
S20	10	S10 and (memory adj dice)	US-PGPUB; USPAT	OR	ON	2007/01/03 18:44
S21	0	S10 and ((memory adj dice) with ("same" adj (side or surface)))	US-PGPUB; USPAT	OR	ON	2007/01/03 18:44
S26	0	((memory adj dice) with (side\$1by\$1side))	US-PGPUB; USPAT	OR	ON	2007/01/03 18:45
S27	0	((memory adj dice) with (side\$1by\$1side))	US-PGPUB; USPAT	OR	ON	2007/01/03 18:45
S24	0	((memory adj dice) with (side adj side))	US-PGPUB; USPAT	OR	ON	2007/01/03 18:45
S25	0	((memory adj dice) with (side adj "by" adj side))	US-PGPUB; USPAT	OR	ON	2007/01/03 18:45
S22	0	((memory adj dice) with ("same" adj (side or surface)))	US-PGPUB; USPAT	OR	ON	2007/01/03 18:45
S30	15	(memory with logic with (side\$1by\$1side))	US-PGPUB; USPAT	OR	ON	2007/01/03 18:46
S29	0	(memory with logic with (side adj "by" adj side))	US-PGPUB; USPAT	OR	ON	2007/01/03 18:46
S34	14	S33 and (memory with logic)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:16
S31	32	(memory with logic with ("same" adj wafer))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:16
S32	0	(memory with die with (uncut or unscribed))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:16
S23	21	((memory adj dice) with (logic))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:16
S39	2	S38 not S35	US-PGPUB; USPAT	OR	ON	2007/01/04 12:19
S35	14	S33 and (memory with logic)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:19
S36	42	((memory or dram or eeprom or eprom or flash or prom or rom) with (uncut or unscribed) not fuse)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:19
S44	11	S43 not (S38 or S35)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:20
S43	14	S42 and ((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) with logic)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:20
S38	3	S37 and ((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) with logic)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:20
S50	0	(((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) near2 wafer) with logic) same (ball or bump or c4))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:24

EAST Search History

S48	0	((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) near2 wafer) with logic with (ball or bump or cr))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:24
S49	0	((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) near2 wafer) with logic with (ball or bump or c4))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:24
S45	0	((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) near2 wafer) with (uncut or unscribed) not fuse)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:24
S55	0	S54 and (memory with (uncut or unscribed))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:27
S58	0	257/777.cor. and ((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) with (uncut or unscribed))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:27
S63	2	S62 and memory	DERWENT	OR	ON	2007/01/04 12:32
S56	0	S54 and ((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) with (uncut or unscribed))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:32
S65	1	S64 and ((((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) near2 wafer) with logic) and (ball or bump or c4))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:33
S51	15	((((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) near2 wafer) with logic) and (ball or bump or c4))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:33
S70	27	S69 not (S51 or S43 or S38 or S35)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:34
S67	0	S66 not S65	US-PGPUB; USPAT	OR	ON	2007/01/04 12:34
S68	28	S64 and ((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) with logic)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:34
S66	1	S64 and ((((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) near2 wafer) with logic))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:34
S53	15	S51 not (S43 or S38 or S35)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:34
S69	28	S64 and ((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) with logic)	US-PGPUB; USPAT	OR	ON	2007/01/04 12:35
S72	9	257/777.fs. and S71	US-PGPUB; USPAT	OR	ON	2007/01/04 12:36
S73	9	257/777.fs. and S71	US-PGPUB; USPAT	OR	ON	2007/01/04 12:36
S57	0	257/777.fs. and ((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) with (uncut or unscribed))	US-PGPUB; USPAT	OR	ON	2007/01/04 12:36
S75	9	257/777.fs. and S74	US-PGPUB; USPAT	OR	ON	2007/01/04 12:38
S77	1	S76 and S74	US-PGPUB; USPAT	OR	ON	2007/01/04 12:39
S79	36	((memory or dram or eeprom or ram or (dynamic adj random) or eprom or flash or prom or rom) with logic with (csp or (chip adj size) or chip\$1size))	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/04 12:43

EAST Search History

S83	33	S81 not S71	US-PGPUB; USPAT	OR	ON	2007/01/04 12:48
S90	14	S88 not S89	US-PGPUB; USPAT	OR	ON	2007/01/04 13:26
S78	1	((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with logic with (csp or (chip adj size) or chip\$1size) with (coc or soc or (system adj chip) or mcm))	US-PGPUB; USPAT	OR	ON	2007/01/04 13:26
S87	0	((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) adj wafer) with (coc or soc or (system adj chip) or mcm))	US-PGPUB; USPAT	OR	ON	2007/01/04 13:26
S89	3	((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) adj wafer) same (coc or soc or (system adj chip) or mcm))	US-PGPUB; USPAT	OR	ON	2007/01/04 13:26
S88	17	((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) adj wafer) and (coc or soc or (system adj chip) or mcm))	US-PGPUB; USPAT	OR	ON	2007/01/04 13:26
S92	0	S91 and ((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) adj wafer) with logic)	US-PGPUB; USPAT	OR	ON	2007/01/05.12:33
S98	14	S97 and ((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with logic)	US-PGPUB; USPAT	OR	ON	2007/01/05 12:34
S96	3	S95 and ((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with logic)	US-PGPUB; USPAT	OR	ON	2007/01/05 12:34
S94	14	S93 and (memory with logic)	US-PGPUB; USPAT	OR	ON	2007/01/05 12:34
S99	15	((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) near2 wafer) with logic) and (ball or bump or c4))	US-PGPUB; USPAT	OR	ON	2007/01/05 12:52
S100	15	S99 not (S98 or S96 or S94)	US-PGPUB; USPAT	OR	ON	2007/01/05 12:53
S59	1	257/777.cor. and ((uncut or unscribed))	US-PGPUB; USPAT	OR	ON	2007/01/05 13:05
S108	13	257/777.cor. and ((intact))	US-PGPUB; USPAT	OR	ON	2007/01/05 13:05
S106	10	((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with ("same" adj surface) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/05 13:06
S114	0	((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with ("not" adj (cut or diced or scribed or sawed)))	US-PGPUB; USPAT	OR	ON	2007/01/05 16:38
S115	0	((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) same ("not" adj (cut or diced or scribed or sawed)))	US-PGPUB; USPAT	OR	ON	2007/01/05 16:38
S122	14	S121 and ((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with logic)	US-PGPUB; USPAT	OR	ON	2007/01/05 16:39
S120	3	S119 and ((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with logic)	US-PGPUB; USPAT	OR	ON	2007/01/05 16:39
S123	15	((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) near2 wafer) with logic) and (ball or bump or c4))	US-PGPUB; USPAT	OR	ON	2007/01/05 16:39

EAST Search History

S11 6	23	(((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) adj wafer) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/05 16:39
S12 9	10	S128 and (bump or ball or c4)	US-PGPUB; USPAT	OR	ON	2007/01/05 17:40
S10 5	27	S104 and (bump or ball or c4)	US-PGPUB; USPAT	OR	ON	2007/01/05 18:01
S11 8	14	S117 and (memory with logic)	US-PGPUB; USPAT	OR	ON	2007/01/05 18:08
S12 8	28	((two adj (memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) adj chips) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	OFF	2007/01/05 18:21
S13 6	37	S135 and (bump or ball or c4)	US-PGPUB; USPAT	OR	ON	2007/01/05 18:22
S11 3	48	S112 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/05 18:23
S13 8	4	((stack or stacked or stacking) with (memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with wafer with (peripheral or logic or support))	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/05 18:28
S14 1	1	S140 and ((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/05 18:29
S13 7	11	S136 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/05 18:30
S13 2	1	S131 and (bump or ball or c4)	US-PGPUB; USPAT	OR	ON	2007/01/05 18:30
S14 4	26	S143 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/05 18:31
S14 5	21	((coc or soc or (system adj chip) or mcm) with (stack or stacked or stacking) with (memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with (peripheral or logic or support)) same wafer	US-PGPUB; USPAT	OR	ON	2007/01/05 18:31
S14 6	21	((coc or soc or (system adj chip) or (chip adj chip) or mcm) with (stack or stacked or stacking) with (memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with (peripheral or logic or support)) same wafer)	US-PGPUB; USPAT	OR	ON	2007/01/05 18:31
S14 7	45	((coc or soc or (system adj chip) or (chip adj chip) or chip\$1on\$1chip or mcm) with (stack or stacked or stacking) with (memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with (peripheral or logic or support)) same wafer)	US-PGPUB; USPAT	OR	ON	2007/01/05 18:35
S15 2	14	S151 not S148	US-PGPUB; USPAT	OR	ON	2007/01/05 18:37
S14 8	14	S147 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/05 18:37
S15 1	28	S150 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/05 18:49
S15 7	7	S156 and ((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/05 18:50

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S15 5	12	S154 not (S151 or S148)	US-PGPUB; USPAT	OR	ON	2007/01/05 18:50
S15 4	40	S153 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/05 19:22
S15 8	2	((wafer adj wafer) or wafer\$1on\$1wafer or chip\$1on\$1wafer or (chip adj wafer) or (wafer adj chip) or (wafer\$1on\$1chip)) same ((stack or stacked or stacking) with (memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with (peripheral or logic or support)))	US-PGPUB; USPAT	OR	ON	2007/01/05 19:22
S16 3	22	S162 not S160	US-PGPUB; USPAT	OR	ON	2007/01/05 19:25
S16 0	24	S159 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/05 19:25
S15 9	44	((wafer adj level) or wafer\$1level) with (memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/05 19:25
S16 4	2	((stack or stacked or stacking) with ((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) adj2 wafer) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/05 19:28
S16 2	22	S161 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/05 19:30
S16 6	4	((coc or soc or (system adj chip) or (chip adj chip) or chip\$1on\$1chip or mcm or (wafer adj wafer) or wafer\$1on\$1wafer or chip\$1on\$1wafer or (chip adj wafer) or (wafer adj chip) or (wafer\$1on\$1chip)) with ((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) adj2 wafer) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/05 19:30
S16 9	39	S168 not (S116 or S126 or S124 or S123 or S122 or S120 or S118)	US-PGPUB; USPAT	OR	ON	2007/01/05 19:31
S12 7	20	S116 not (S126 or S124 or S123 or S122 or S120 or S118)	US-PGPUB; USPAT	OR	ON	2007/01/05 19:31
S17 2	0	((dram or (dynamic adj (random or ram))) with (unitary adj wafer) with logic)	US-PGPUB; USPAT	OR	ON	2007/01/06 15:27
S17 3	0	((dram or (dynamic adj (random or ram))) with (unitary adj wafer))	US-PGPUB; USPAT	OR	ON	2007/01/06 15:27
S17 5	0	((dram or (dynamic adj (random or ram))) with ((whole or entire or complete or portion) adj wafer) with (bond or bonding or bonded))	US-PGPUB; USPAT	OR	ON	2007/01/06 15:28
S17 6	6	((dram or (dynamic adj (random or ram))) with ((whole or entire or complete or portion) adj wafer) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 15:29
S17 9	22	S178 and (coc or soc or (system adj chip) or (chip adj chip) or chip\$1on\$1chip or mcm or (wafer adj wafer) or wafer\$1on\$1wafer or chip\$1on\$1wafer or (chip adj wafer) or (wafer adj chip) or wafer\$1on\$1chip)	US-PGPUB; USPAT	OR	ON	2007/01/06 15:30
S17 4	47	((dram or (dynamic adj (random or ram))) with ((whole or entire or complete or portion) adj wafer))	US-PGPUB; USPAT	OR	ON	2007/01/06 15:30
S17 8	47	((dram or (dynamic adj (random or ram))) with ((whole or entire or complete or portion or unitary) adj wafer))	US-PGPUB; USPAT	OR	ON	2007/01/06 15:30

EAST Search History

S16 5	0	((coc or soc or (system adj chip) or (chip adj chip) or chip\$1on\$1chip or mcm or (wafer adj wafer) or wafer\$1on\$1wafer or chip\$1on\$1wafer or (chip adj wafer) or (wafer adj chip) or (wafer\$1on\$1chip)) with (stack or stacked or stacking) with ((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) adj2 wafer) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 15:30
S17 7	9	(((dram or (dynamic adj (random or ram))) adj2 wafer) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 15:51
S18 1	3	((dram or (dynamic adj (random or ram))) with (wafer\$1scale or (wafer adj scale)) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 16:46
S18 4	24	S183 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/06 16:47
S18 0	21	S179 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/06 16:47
S18 2	1	((dram or (dynamic adj (random or ram))) same (memory with (wafer\$1scale or (wafer adj scale)) with (peripheral or logic or support)))	US-PGPUB; USPAT	OR	ON	2007/01/06 16:47
S18 3	24	((dram or (dynamic adj (random or ram))) and (memory with (wafer\$1scale or (wafer adj scale)) with (peripheral or logic or support)))	US-PGPUB; USPAT	OR	ON	2007/01/06 17:00
S18 5	2	((dram or (dynamic adj (random or ram))) and (memory with (wafer\$1scale or (wafer adj scale)) with (peripheral or logic or support)))	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 17:00
S17 0	4	((memory or dram or eeprom or ram or (dynamic adj random) or eeprom or flash or prom or rom) with (wafer near (bond or bonding or bonded)) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 17:01
S18 6	3	((dram or (dynamic adj (random or ram)))with (wafer near (bond or bonding or bonded)) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 17:02
S19 0	1	(memory with (wafer near (bond or bonding or bonded)) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 17:03
S18 9	3	((dram or (dynamic adj (random or ram))) with (wafer near (bond or bonding or bonded)) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 17:03
S19 3	0	S191 and ((memory adj wafer) with (bond or bonding or bonded) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 17:05
S19 4	0	((memory adj wafer) with (bond or bonding or bonded) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 17:05
S19 6	21	(((dram or (dynamic adj (random or ram))) near3 (bond or bonding or bonded)) with wafer)	US-PGPUB; USPAT	OR	ON	2007/01/06 17:29
S19 2	3	S191 and ((dram or (dynamic adj (random or ram))) with (bond or bonding or bonded) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 17:30
S20 0	3	S198 and ((dram or (dynamic adj (random or ram))) with wafer with (stack or stacked or stacking))	US-PGPUB; USPAT	OR	ON	2007/01/06 18:38
S20 2	16	(((dram or (dynamic adj (random or ram))) with wafer with (stack or stacked or stacking)) same (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/06 18:43

EAST Search History

S20 7	0	((dram or (dynamic adj (random or ram))) with wafer with (stack or stacked or stacking)) and 257/773-781.fs.	US-PGPUB; USPAT	OR	ON	2007/01/06 19:02
S20 6	0	((dram or (dynamic adj (random or ram))) with wafer with (stack or stacked or stacking)) and 257/773-780.fs.	US-PGPUB; USPAT	OR	ON	2007/01/06 19:02
S20 9	0	((dram or (dynamic adj (random or ram))) with wafer with soc) and 257/773-781.ccls.	US-PGPUB; USPAT	OR	ON	2007/01/06 19:05
S20 8	3	((dram or (dynamic adj (random or ram))) with wafer with (stack or stacked or stacking)) and 257/773-781.ccls.	US-PGPUB; USPAT	OR	ON	2007/01/06 19:05
S21 0	8	((dram or (dynamic adj (random or ram))) with wafer with soc)	US-PGPUB; USPAT	OR	ON	2007/01/06 19:06
S21 7	9	(((dram or (dynamic adj (random or ram))) adj2 wafer) with (logic or peripheral or support))	US-PGPUB; USPAT	OR	ON	2007/01/18 12:14
S19 9	28	S198 and ((dram or (dynamic adj (random or ram))) with (peripheral or logic or support))	US-PGPUB; USPAT	OR	ON	2007/01/18 12:15
S12	36	S11 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/01/18 12:15
S22 1	31	S219 and ("257"/\$3.cor.) or ("438"/\$3.cor.)	US-PGPUB; USPAT	OR	ON	2007/01/18 12:19
S22 8	0	S227 and ((memory adj (chip or wafer or substrate)) with ((logic or peripheral or support) adj (chip or wafer or substrate)))	US-PGPUB; USPAT	OR	ON	2007/01/18 12:29
S22 5	37	((memory adj (chip or wafer or substrate)) with ((logic or peripheral or support) adj (chip or wafer or substrate)) with (bump or ball))	US-PGPUB; USPAT	OR	ON	2007/01/18 12:43
S23 2	0	((memory adj (wafer)) with ((logic or peripheral or support) adj (chip or wafer or substrate)) with (bump or ball))	US-PGPUB; USPAT	OR	ON	2007/01/18 12:43
S23 3	2	((memory adj (wafer)) with ((logic or peripheral or support) adj (chip or wafer or substrate)))	US-PGPUB; USPAT	OR	ON	2007/01/18 12:43
S21 6	0	(((dram or (dynamic adj (random or ram))) adj2 wafer) with (bond or bonded or bonding or mount or mounted or mounting) with (logic or peripheral or support))	US-PGPUB; USPAT	OR	ON	2007/02/22 18:30
S23 4	0	(((dram or (dynamic adj (random or ram))) adj2 wafer) with (bond or bonded or bonding or mount or mounted or mounting) with (logic or peripheral or support))	US-PGPUB; USPAT	OR	ON	2007/02/22 18:30
S23 8	0	((memory adj (chip or wafer or substrate)) with ((whole or entire) adj (logic or peripheral or support) adj (chip or wafer or substrate)) with (bump or ball))	US-PGPUB; USPAT	OR	ON	2007/02/28 16:47
S23 9	0	((memory adj (chip or wafer or substrate)) with ((whole or entire) adj (logic or peripheral or support) adj (chip or wafer or substrate)))	US-PGPUB; USPAT	OR	ON	2007/02/28 16:47
S24 1	0	((memory adj (wafer)) with ((logic or peripheral or support) adj (chip or wafer or substrate)) with (bump or ball))	US-PGPUB; USPAT	OR	ON	2007/02/28 16:48
S24 3	0	((memory adj (wafer)) with ((logic or peripheral or support) adj (chip or wafer or substrate)))	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/28 16:48
S24 0	4	((memory adj (chip or wafer or substrate)) with ((logic or peripheral or support) adj (wafer)))	US-PGPUB; USPAT	OR	ON	2007/02/28 16:48

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S23 7	37	((memory adj (chip or wafer or substrate)) with ((logic or peripheral or support) adj (chip or wafer or substrate)) with (bump or ball))	US-PGPUB; USPAT	OR	ON	2007/02/28 16:48
S24 4	1	((memory adj (wafer)) same ((logic or peripheral or support) adj (chip or wafer or substrate)))	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/28 16:51
S24 6	1	((memory adj (wafer)) and ((logic or peripheral or support) adj (chip or wafer or substrate)))	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/28 16:51
S24 8	3	((memory adj (wafer)) with ((dicing or dice or scribing or scribe) adj (line or area)))	US-PGPUB; USPAT	OR	ON	2007/02/28 16:52
S24 7	2	((memory adj (wafer)) same ((logic or peripheral or support) adj (chip or wafer or substrate)))	US-PGPUB; USPAT	OR	ON	2007/02/28 16:52
S24 2	2	((memory adj (wafer)) with ((logic or peripheral or support) adj (chip or wafer or substrate)))	US-PGPUB; USPAT	OR	ON	2007/02/28 16:55
S25 1	0	((whole or entire) adj2 (dram or eeprom or eprom or flash) adj wafer)	US-PGPUB; USPAT	OR	ON	2007/02/28 16:56
S24 9	0	((whole or entire) adj memory adj wafer)	US-PGPUB; USPAT	OR	ON	2007/02/28 16:56
S25 0	1	((whole or entire) adj2 memory adj wafer)	US-PGPUB; USPAT	OR	ON	2007/02/28 17:18
S25 4	4	(coc with (memory near2 wafer))	US-PGPUB; USPAT	OR	ON	2007/02/28 17:23
S25 7	6	S256 and (memory near2 wafer)	US-PGPUB; USPAT	OR	ON	2007/02/28 17:24
S25 8	2	S257 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/02/28 17:25
S23 6	9	S235 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/02/28 17:25
S23 5	37	((memory adj (chip or wafer or substrate)) with ((logic or peripheral or support) adj (chip or wafer or substrate)) with (bump or ball))	US-PGPUB; USPAT	OR	ON	2007/02/28 18:44
S26 3	37	((memory adj (chip or wafer or substrate)) with ((logic or peripheral or support) adj (chip or wafer or substrate)) with (bump or ball))	US-PGPUB; USPAT	OR	ON	2007/02/28 18:51
S25 2	5	((whole or entire) with ((dram or eeprom or eprom or flash) adj2 wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 17:41
S22 6	9	S225 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/03/02 18:10
S26 5	37	((memory adj (chip or wafer or substrate)) with ((logic or peripheral or support) adj (chip or wafer or substrate)) with (bump or ball))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:10
S26 9	4	S268 and wafer	US-PGPUB; USPAT; USOCR	OR	ON	2007/03/02 18:17
S26 4	5	((whole or entire) with ((dram or eeprom or eprom or flash) adj2 wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:26
S27 4	0	(solderede with ((dram or eeprom or eprom or flash or memory) adj wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:27
S27 5	0	(soldered with ((dram or eeprom or eprom or flash or memory) adj wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:27

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S27 7	2	(solder with ((dram or eeprom or eprom or flash or memory) adj2 wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:27
S27 1	5	(bonded with ((dram or eeprom or eprom or flash memory) adj wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:27
S27 3	0	(ball with ((dram or eeprom or eprom or flash or memory) adj wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:27
S27 2	0	(ball with ((dram or eeprom or eprom or flash memory) adj wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:27
S28 0	6	S278 and soc	US-PGPUB; USPAT	OR	ON	2007/03/02 18:28
S27 9	0	S278 and coc	US-PGPUB; USPAT	OR	ON	2007/03/02 18:28
S27 6	0	(solder with ((dram or eeprom or eprom or flash or memory) adj wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:28
S28 2	3	S281 and (((dram or eeprom or eprom or flash or memory) adj wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:31
S27 0	3	S268 and wafer and memory	US-PGPUB; USPAT; USOCR	OR	ON	2007/03/02 18:31
S28 3	14	S281 and (((dram or eeprom or eprom or flash or memory) adj2 wafer))	US-PGPUB; USPAT	OR	ON	2007/03/02 18:52
S28 5	2	S281 and (stack with (dram or eeprom or eprom or flash or memory) with wafer with logic)	US-PGPUB; USPAT	OR	ON	2007/03/02 18:53
S26 6	9	S265 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/03/02 18:53
S28 9	1	S288 and ((dram or eeprom or eprom or flash or memory) with wafer with logic)	US-PGPUB; USPAT	OR	ON	2007/03/02 18:57
S28 7	18	S286 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/03/02 18:57
S29 0	6	S288 and ((dram or eeprom or eprom or flash or memory) with logic)	US-PGPUB; USPAT	OR	ON	2007/03/02 18:58
S29 2	12	S281 and ((dram or eeprom or eprom or flash or memory) with logic with wafer)	US-PGPUB; USPAT	OR	ON	2007/03/02 19:24
S29 4	6	((dram or eeprom or eprom or flash or memory or (non adj volatile) or non\$1volatile) with (logic or peripheral or support) with wafer with ball)	US-PGPUB; USPAT	OR	ON	2007/03/02 19:31
S29 6	22	((dram or eeprom or eprom or flash or memory or (non adj volatile) or non\$1volatile) with (logic or peripheral or support) with wafer with (stack or stacked or stacking) not (die or dice))	US-PGPUB; USPAT	OR	ON	2007/03/02 19:32
S30 2	5	S300 and ((dram or eeprom or eprom or flash or memory or (non adj volatile) or non\$1volatile) with wafer)	US-PGPUB; USPAT	OR	ON	2007/03/02 19:33
S30 1	2	S300 and ((dram or eeprom or eprom or flash or memory or (non adj volatile) or non\$1volatile) with (logic or peripheral or support) with wafer)	US-PGPUB; USPAT	OR	ON	2007/03/02 19:46
S30 5	13	S304 and ((dram or eeprom or eprom or flash or memory or (non adj volatile) or non\$1volatile) with (logic or peripheral or support) with wafer)	US-PGPUB; USPAT	OR	ON	2007/03/02 19:47
S30 6	21	S304 and ((dram or eeprom or eprom or flash or memory or (non adj volatile) or non\$1volatile) with (logic or peripheral or support))	US-PGPUB; USPAT	OR	ON	2007/03/02 19:47

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S30 7	8	S306 and (@ad<"20010711" or @rlad<"20010711")	US-PGPUB; USPAT	OR	ON	2007/03/02 19:48
S30 9	2	S308 and wafer	US-PGPUB; USPAT	OR	ON	2007/03/02 19:49
S30 8	5	S307 not S305	US-PGPUB; USPAT	OR	ON	2007/03/02 19:49